

Notice of References Cited	Application/Control No. 10/018,180	Applicant(s)/Patent Under Reexamination OFFENBERG ET AL.	
	Examiner Matthew A. Anderson	Art Unit 1765	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,067,858	05-2000	Clark et al.	73/504.16
*	B	US-5,929,497	07-1999	Chavan et al.	257/417
*	C	US-5,075,253	12-1991	Sliwa, Jr., John W.	438/109
*	D	US-6,689,694 B1	02-2004	Cho et al.	438/696
*	E	US-6,328,794 B1	12-2001	Brouillette et al.	117/4
*	F	US-6,150,275	11-2000	Cho et al.	438/696
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	M	US-			

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al., Silicon Processing for the VLSI Era Volume 1: Processing Technology, Lattice Press, sunset Beach, CA, USA, pp. 1151-156, 1986.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

Matthew A. Anderson 1/5/2005